## Microprocessor Systems and Applications: EC 606 EC B. E. III ( EC) 6<sup>th</sup> Sem.

		Lecture	Tutorial	Practio	cal	
	Teaching Hours	3	1	2		
	Examination Scheme Marks	100	25	Cont. Evalua	ation : 20	
	Ecoulty: Mc Justi Agroup	l & Mr. Diguijou	Dethod	Examination	: 30	
LecNo	Content	i & Mil. Digvijay	Katilou		Text	Ref
1	Introduction to 16 bit Micro	processor			[1]	[3]
2	8086 internal Architecture Signal Description of 8086 Internal				$Ch \cdot 2$	[3]
2	execution and timing	, Signai De	semption of 0	ooo, memar	P: 19-36	[5]
3	Flag register. Concept of segmentation				[3]	
4	Introduction to 8086 Fami	[1]	[3]			
	data types.	5	0 0	6 6	Ch : 3	
5	Data transfer instructions,	Arithmetic ins	structions, Logi	cal, shift and	P:42-64	[3]
	rotate instructions, Branch in	nstructions, Lo	oop instructions	5.	Ch : 6	
		,	1	,	P:131-162	
6	Program for addition, average	ge, ascending/	descending		[3]	[6]
7	Program for Fibonacci serie	s 25 elements	(4 digit)		[3]	[6]
	Holiday					
8	Procedure call & stack oper	ation, program	n of number co	nversion with	[1]	[6]
	procedure				Ch: 4 & 5	
9	NOP,HLT and flag manipul	ation instructi	ons		P:65-122	[3]
10	Assembler directives					[3]
11	Machine language instruction	ons, String Ins	truction			[3]
	Holiday					
12	Logic & Program of 32 & 6	4 bit number r	nultiplication.		[1] P: 130	[6]
	Holiday					
13	Programming with DOS & I	<b>BIOS</b> function	n calls.		[3]	[6]
14	Logic & Program of division	n 64 bit (Divid	dend) /16 bit (di	ivisor)	[1]	[6]
					P :123-126	
15	Programming with DOS & I	BIOS function	n calls.		[3]	[6]
16	Programming with string ins	structions			[3]	[6]
17	Programming with string ins	structions			[3]	[6]
18	Program to enter/read & dis	play strings/ni	umbers on scree	en.	[3]	[6]
19	PROGRAMMING WITH M	<b>1ACROS</b>			[1]	[6]
					P:127-129	
20	TEST OF ALL INSTRUCT	IONS			[1] Ch : 6	503
21	8086 System Connections	And Timings	: 8086 Hardw	are overview,		[3]
	Basic signal flow on 8086 b	uses			CH:7	501
22	Analyzing a minimum mode	<u>system</u>				[3]
23	8086 addressing and address	s decoding,				[3]
24	8086 timing parameters					[3]
	Kshitij					
	Kshitij					
25	8086 Maximum Mode & co	mplete system	1			[3]
	Interrupts And Interrupt	Service Proce	edures :			
26	8086 interrupts and interrup	t responses, 8	086 Interrupt ty	pes, Interrupt	[1]	[3]
	vector table				Ch : 8	

27	Interrupt Program Example		[3]
28	Hardware and software considerations for using interrupts, priority of		[3]
	interrupts		
29	Example for counting applications		[3]
30	8254 Modes & interrupt application		[3]
31	8259A Priority Interrupt Controller		[3]
32	8086 example interrupt		[3]
	I/O Programming :		
33	Fundamental I/O considerations,	[2]	
34	Programmed and Interrupt I/O,		
35	Block transfers and DMA		
36	I/O design example		
37	Test		
	Interfacing :		
38	Programmable parallel ports and handshake input/output,	[3]	[1,5]
39	Interfacing microprocessors to keyboard and displays,		[1,5]
40	D/A converter operation, Interfacing and applications, A/D converter		[1,5]
	interfacing,		
41	Stepper motor Interfacing,		[1,5]
42	Serial communication interfaces UART, USART, General		[1,5]
	Applications.		
43	Examples Interfacing		[1,5]
44	Examples Interfacing		[1,5]
	Introduction to Microcontrollers :		
45	Introduction to Microcontrollers	[7]	[8]
46	8051 architecture overview		
47	8051 I/O ports, timer, counters, interrupts etc		
48	Programming methodology & tools of 8051 microcontroller		
49	8051 Instructions		
50	8051 Instructions	]	
51	8051 Programming		
52	8051 Programming		
53	8051 Applications		
54	Revision		
55	Revision		

Reference:

- 1. Hall Douglas V: Microprocessors and Interfacing, Programming and Hardware (TMH)
- 2. Gibson Glenn A. and Liu Yu Cheng: Microcomputer Systems: The 8086/8088 Family, Architecture, Programming and Design (PHI 2<sup>nd</sup> Ed)
- 3. A K Ray & K M Bhurchandi : Advanced Microprocessors & Peripherals : Architecture, Programming & Interfacing, TMH
- 4. John Uffenbeck : The 8086/8088 family : Design Programming & Interfacing, PHI
- 5. James L Antonakos : An Introduction to the Intel family of Microprocessors, Pearson Education (LPE), 3<sup>rd</sup> Ed
- 6. Peter Abel : IBM PC Assembly language & Programming, PHI, 3<sup>rd</sup> Ed
- 7. Kenneth J Ayala : The 8051 Microcontroller Architecture, Programming & Applications, Penram International, 2<sup>nd</sup> Ed.

8. Microcontroller by Mazidi, Pearson Education